

RECEIVED  
CENTRAL FAX CENTER

Application No. 10/734,176  
Amendment dated September 13, 2006  
Reply to Office Action of June 13, 2006

SEP 13 2006

Docket No.: 3313-1080P

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) An assisted generating A file conversion system for layout data conversions to convert layout data in converting an output file generated by from a circuit design program (such as ConceptHDL) into a converted file to help actual layout designs of for a circuit layout program (such as Allegro), which system comprises comprising:

a property database, which stores property definitions of a plurality of fields corresponding to a unified set of names;

a rule database, which stores rule definitions of a plurality of fields corresponding to the unified set of names;

a conversion module, which analyzes contents of the output file to generate a plurality of fields, extracts the corresponding rule definitions using the unified set of names for performing settings, and adjusts property definitions of all of the fields using the unified set of names to produce a converted file; and

a user interface (UI), which displays all of the fields along with the corresponding property definitions and rule definitions.

2. (Original) The system of claim 1, wherein the conversion module further highlights the adjusted fields.

3. (Currently Amended) The system of claim 1, wherein the UI further provides editing commands for the user to edit the displayed fields, property definitions, and rule definitions.

Application No. 10/734,176  
Amendment dated September 13, 2006  
Reply to Office Action of June 13, 2006

Docket No.: 3313-1080P

4. (Original) The system of claim 1, wherein the UI uses an interface compatible with a spread sheet program.

5. (Currently Amended) ~~An assisted generating A file conversion method for layout data conversions to convert layout data in converting an output file from generated by a circuit design program (such as ConceptHDL) into a converted file to help actual layout designs of for a circuit layout program (such as Allegro), which method comprises the steps of comprising:~~

obtaining the output file for analysis;

executing a plurality of field conversions of the output file according to a unified set of names;

adjusting properties, setting rules according to the unified set of names, and displaying the results in a user interface (UI); and

outputting a converted file for the circuit layout program subsequent layout processes.

6. (Original) The method of claim 5, wherein the UI uses an interface compatible with a spread sheet program.

7. (Original) The method of claim 5, wherein the step of adjusting properties, setting rules, and displaying the results in a UI further comprises the steps of:

reading in the fields and extracting the corresponding rules for performing settings;

checking whether the property of each of the fields is abnormal; and

Application No. 10/734,176  
Amendment dated September 13, 2006  
Reply to Office Action of June 13, 2006

Docket No.: 3313-1080P

displaying all of the fields on the UI after they are checked.

8. (Original) The method of claim 87, wherein the step of checking whether the property of each of the fields is abnormal comprises the steps of:

extracting correct properties to adjust the fields; and

highlighting the corrected fields.

9. (New) The system of claim 1, wherein the property database includes the unified set of names and the corresponding attributes of the circuit elements.

10. (New) The system of claim 1, wherein the conversion module generates the converted file by extracting the attributes of circuit elements from the property database by comparing names of circuit elements.

11. (New) The method of claim 5, further comprising the step of generating the converted file by extracting the attributes of circuit elements from the property database by comparing names of circuit elements.